PLSY PLSR ZRN DRVI DRVA
Instructions for use of equal pulse sending and positioning instructions

1) The maximum pulse transmission support is Y0, Y1, Y2, Y3, Y4, Y5, Y6, Y7, with the maximum of 200 kHz for Y0-Y5 and Y6-Y7 simultaneously.Up to 100 Khz special auxiliary relay

Y0	Y1	Y2	Y3	Y4 *α	Y5 *α	Y6 *α	Y7 *α	name	read and write	Object instruction
M8029								End of instruction execution flag bit	R	PLSY/PLSR/DSZR/ DV IT/ZRN/DRVI/ DRVA wait for
M8329								Instruction execution abnormal end flag bit	R	PLSY/PLSR/DSZR/ DVIT/ZRN/PLSV/ DRVI/DRVA
M8338								Acceleration and deceleration action	R/W	PLSV
M8336								The interrupt command function is valid.	R/W	DVIT
M8340	M8350	M8360	M8370	M8440	M8450	M8470	M8480	Monitoring in pulse output	R	PLSY/PLSR/DSZR/ DVIT/ZRN/PLSV/ DRVI/DRV A

Y0	Y1	Y2	Y3	Y4*α	Y5*α	Y6*α	Y7*α	name	read and write	Object instruction
M8341	M8351	M8361	M8371	M8441	M8451	M8471	M8481	Clear signal output function is effective*β	R/W	DSZR/ZRN
M8342	M8352	M8362	M8372	M8442	M8452	M8472	M8482	Origin regression direction instruction	R/W	DSZR
M8343	M8353	M8363	M8373	M8443	M8453	M8473	M8483	Positive limit	R/W	PLSY/PLSR/DSZR/ DVIT/ZRN/PLSV/ DRVI/DRVA
M8344	M8354	M8364	M8374	M8444	M8454	M8474	M8484	Reversal limit		
M8345	M8355	M8365	M8375	M8445	M8455	M8475	M8485	Logic inversion of near-point signal	R/W	DSZR
M8346	M8356	M8366	M8376	M8446	M8456	M8476	M8486	Logic inversion of zero point signal		

Y0	Y1	Y2	Y3	Y4*α	Y5*α	Y6*α	Y7*α	name	read and write	Object instruction
M8347	M8357	M8367	M8377	M8447	M8457	M8477	M8487	Interrupt signal logic inversion	R/W	DVIT
M8348	M8358	M8368	M8378	M8448	M8458	M8478	M8488	Positioning command driven	R	PLSY/PWM/PLSR/ DSZR/DVIT/ZRN/ PLSV/DRVI/DRVA
M8349	M8359	M8369	M8379	M8449	M8459	M8479	M8489	Pulse stop instruction	R/W	PLSY/PLSR/DSZR/ DVIT/ZRN/PLSV/ DRVI/DRVA
M8460	M8461	M8462	M8463	M8152	M8153	M8154	M8155	User interrupt input instruction	R/W	DVIT
M8464	M8465	M8466	M8467	M8156	M8157	M8158	M8159	The specified function of clearing signal soft element is valid.	R/W	DSZR/ZRN

* α When the high-speed output of the software is 2-4 axes, and the specified function of the reset signal soft component is invalid, the reset signals Y0-Y4,Y1-Y5,Y2-Y6,Y3-Y7,
* β When the high-speed output of the software is 6-8 axes, and the specified function of the reset signal soft component is invalid, the reset signals Y0-Y10,Y1-Y11,Y2-Y12,Y3-Y13,Y4-Y14, Y5-Y15,Y6-Y16,Y7-Y17

Soft component number								name	length	initial value	Object instruction
Y0	Y1	Y2	Y3	Y4*α	Y5*α	Y6*α	Y7*α				
D8336				D8337				Interrupt instruction	16-bit	0	DVIT
D8340	D8350	D8360	D8370	D8440	D8450	D8470	D8480	Current value register	32-bit	0	DSZR/DVIT/ZRN/ DSZR/ PLSV/DRVI/DRVA
D8341	D8351	D8361	D8371	D8441	D8451	D8471	D8481				
D8342	D8352	D8362	D8372	D8442	D8452	D8472	D8482	Base low speed [Hz]	16-bit	0	
D8343	D8353	D8363	D8373	D8443	D8453	D8473	D8483	maximum speed [Hz]	32-bit	100000	
D8344	D8354	D8364	D8374	D8444	D8454	D8474	D8484				
D8345	D8355	D8365	D8375	D8445	D8455	D8475	D8485	Crawling speed [Hz]	16-bit	1000	DSZR
D8346	D8356	D8366	D8376	D8446	D8456	D8476	D8486	Return velocity of origin[Hz]	32-bit	50000	
D8347	D8357	D8367	D8377	D8447	D8457	D8477	D8487				

Soft component number								name	length	initial value	Object instruction
Y0	Y1	Y2	Y3	Y4*α	Y5*α	Y6*α	Y7*α				
D8348	D8358	D8368	D8378	D8448	D8458	D8478	D8488	Acceleration time [ms]	16-bit	100	DSZR/DVIT/ZRN/ PLSV*β/DRVI /DRVA
D8349	D8359	D8369	D8379	D8449	D8459	D8479	D8489	deceleration time [ms]	16-bit	100	
D8464	D8465	D8466	D8467	D8156	D8157	D8158	D8159	Clear signal soft component assignment	16-bit	0	DSZR/ZRN
D8140	D8142	D8144	D8146	D8170	D8172	D8174	D8176	Pulse current value register	32-bit	0	PLSV/ PLSR*δ
D8141	D8143	D8145	D8147	D8171	D8173	D8175	D8177				

* α high-speed output enhanced function;

* β M8338 needs to be turned ON, and the acceleration and deceleration function of PLSV is effective.

*δ When PLSY and PLSR instructions are used for this pulse, the pulses sent by the shaft accumulate in the corresponding registers.